FIG.1 PRIOR ART

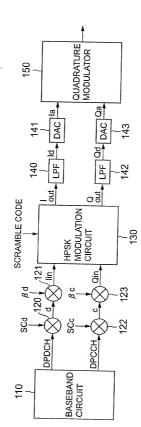


FIG.2

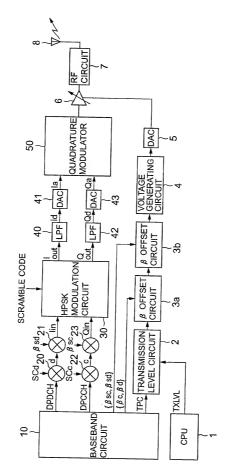
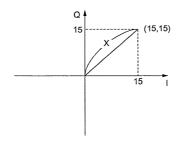


FIG.3

LOGIC	VALUE	SET VALUE						
βс	βd	β sc	β sd					
15	15	15	15					
15	14	15.5	14.6					
15	13	16.0	13.9					
15	12	16.6	13.3					
15	11	17.1	12.5					
15	10	17.7	11.8					
15	9	18.2	10.9					
15	8	18.7	10.0					
15	7	19.2	9.0					
15	6	19.7	7.9					
15	5	20.1	6.7					
15	4	20.5	5.5					
15	3	20.8	4.2					
15	2	21.0	2.8					
15	1	21.2	1.4					

FIG.4



- 20 Carlo Live to the first of the control of the

DIFFERENCE	[dB]	[0	0.30400	-0.39423	-0.39423	-0.035745	-0.31633	0.70	-0.16459	0 16/50	201.0	-0.45359	-0.18327	0 40007	-0.1832/	-0.39423	-0.01798	9,7,0	-0.146	-0.2398	-0.207.04	
	_	β sd4²		242		244		225	200	_	233	+	. 812	232	+	+	. 1.77	241	100	+	- 677	226	
RESSION	0 2 14	β SG4	7	10	5	2 4	10		α		œ	-	-	9	9) 4	0	4	٣		7	-	
SET VALUE (4-BIT EXPRESSION)	β sc4		11	7	11	12	71	12	13	2	13	13	2	14	14	14		15	15	τ ²	2	15	
SET VALUE	Rsd	2	15	14.6	13.9	13.3	2.5	12.5	11.8	0.07	10.9	10.0		9.0	7.9	6.7	i i	0.0	4.2	28		1.4	
SET	βsc	75	0	15.5	16.0	16.6	117	1.7.1	17.7	70.0	7.01	18.7	40.5	13.2	19.7	20.1	20.5	20.04	20.8	21.0	24.0	7.12	
LOGIC VALUE	βd	15	2 ;	14	13	12	17	= :	10	σ	9	00	7	,	9	2	4	. (9	2	-		
LOGIC	βс	15	2	2	15	15	15	45	13	15		15	15		15	15	15	75	0	15	15		

FIG.6A

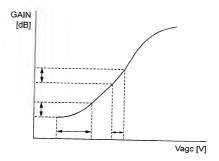
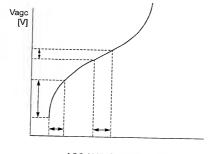


FIG.6B



AGC AMP. CONTROL CODE

FIG.7

